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IMPROVING POWER SYSTEM QUALITY USING FLEXIBLE AC TRANSMISSION SYSTEMS*

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ABSTRACT

This paper presents detailed fast control characteristics and continuous compensation capability. Flexible AC Transmission System (FACTS) devices have been researched and adopted in power engineering area. There are so many advantages of FACTS device; it can increase dynamic stability, loading capability of lines, improve power quality and system security. It can also increase utilization of lowest cost generation. The key role of FACTS device is to control the power flow actively and effectively. In other words, it can transfer power flow from one line to another within its capability.

This paper presents detailed improving power system quality using advanced flexible AC transmission systems (FACTS) controllers, namely, Advanced Thyristor Controlled Series Capacitors (ATCSCs), Advanced Thyristor Controlled Series Reactor (ATCSR), Advanced Static Var Compensator (ASVC) and Advanced Static Var System (ASVS). This paper focuses on the operation of the FACTS device under generator fault that may cause any other transmission lines to be overflowed. The proposed algorithm in this paper is tested on the IEEE 30 bus system.

KEY WORDS: FACTS, ATCSCs, ATCSR, ASVC, ASVS, IEEE 30 bus system.

AMELIORATION DE LA QUALITE SYSTEME D'ALIMENTATION UTILISATION FLEXIBLE CA SYSTÈMES DE TRANSMISSION

RÉSUMÉ

Cet article présente les caractéristiques détaillées de contrôle rapide et la capacité de compensation continue; Flexible AC Transmission System (FACTS) dispositifs ont été étudiés et adoptés dans la zone de l'ingénierie électrique. Il ya tant d'avantages du dispositif de FACTS, il peut augmenter la stabilité dynamique, capacité de chargement de lignes, d'améliorer la qualité de l'énergie et la sécurité du système. Il peut également accroître l'utilisation de la génération la moins coûteuse. Le rôle clé du dispositif de FACTS est de contrôler le flux de puissance active et efficace. En d'autres termes, il peut transférer des flux de puissance d'une ligne à une autre au sein de sa capacité.

Ce document présente en détail améliorer la qualité du système d'alimentation à l'aide de pointe CA flexible des systèmes de transmission (FACTS) contrôleurs, à savoir, avancée Condensateurs Thyristor série contrôlée (ATCSCs), Advanced Thyristor Controlled Series Reactor (ATCSR), Advanced statique Var Compensateur (ASVC) et Advanced Var statique Système (ASVS). Ce document se concentre sur le fonctionnement des dispositifs faits sous la faute génératrice qui peut causer des lignes de transmission d'autres d'être débordé. L'algorithme proposé dans ce papier est testé sur le système de bus IEEE 30.

MOTS CLÉS: FAITS, ATCSCs, ATCSR, ASVC, ASVS, SYSTÈMES BUS IEEE 30.

* Received: 26/2/2012, Accepted:18/3/2012 (Review Paper)
(republished from Vol. 14, No. 2, May 2011, with additional author)

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1. INTRODUCTION:

Studies of power quality phenomena have emerged as an important subject in recent years due to renewed interest in improving the quality of the electricity supply. As sensitive electronic equipment continues to proliferate, the studies of power quality will be further emphasized [1].

There are two main means for improving power quality:

- The cost-free improving power quality.
- Not cost-free improving power quality.

The cost-free means for improving power quality include actions like:

- Operation of tap changing transformers.
- Operation of conventional compensating devices.
- Control by FACTS devices.

Flexible AC Transmission Systems abbreviated by FACTS forms a new domain in power system control engineering, using power electronic devices and circuits and the more recent available technologies in automatic control and valves.

The two main objectives of FACTS are:

1. To increase transmission capacity of lines.
2. Control power flow over designated transmission, electronically and statically, without need of operator's actions and without need of mechanical manipulations or conventional breakers switching.

The collapse points are known as maximum loadability points, the voltage collapse problem can be restated as an optimization problem where the objective is to maximize certain system parameters typically associated to load levels [2, 3, 4, and 5]. Hence, voltage collapse techniques may also be used to compute the maximum power that can be transmitted through the transmission system, also known in the new competitive energy market as Total Transfer Capability (TTC) [6]. It is well known that shunt and series compensation can be used to increase the maximum transfer capabilities of power networks [7]. With the improvements in current voltage handling capabilities of power electric devices that have allowed for the development of FACTS, the possibility has arisen of using different types of controllers for efficient shunt and series compensation. Thus, FACTS controllers based on thyristor controlled reactors (TCRs), such as Thyristor Controlled Series Reactors (TCSRs) and Thyristor

Controlled Series Capacitors (TCSCs), are being used by several utilities to compensate their systems [8]. More recently, various types of controllers for shunt and series compensation, based on voltage source inverters (VSIs), Shunt and Series Static Synchronous Compensators (STATCOMs and SSSCs) and Unified Power Flow Controllers (UPFCs), have been proposed and developed [9].

In [10], the authors used standard voltage collapse analysis tools to study the effect of the maximum load margin on the location of a given SVC; an approximate SVC model is used for the computations. In [11], the authors used approximate SVC and TCSC models together with typical collapse computational tools and optimization techniques to determine the appropriate location and size of these controllers; dynamic simulations using more detailed models are then performed to study the effect of these controllers in the overall stability of the network. In [12] power quality is related to balancing of the unbalanced three-phase load voltages and line currents, while improving load power-factors to unity and performing load voltages regulation, this can be achieved by using (FACTS). In [13] an approach to locate, and size the Static Var Compensator (SVC) using modal analysis in a power system in order to increase the Steady State Voltage Stability (SSVS) margin.

The proposed algorithm in this paper is tested and verified on the IEEE 30 bus system by using ATP simulation program.

2. CONSIDERATION ON POWER QUALITY

The nonlinear characteristics of various office and industrial equipment connected to the power grid could cause electrical disturbances leading to poor power quality. These electrical disturbances could destroy certain sensitive equipment connected to the grid or in some cases could cause them to malfunction. There is virtually no piece of office or industrial equipment that does not depend on electricity in some form or other. Among the office equipment are computers, fax machines, copiers, and telephones etc. Computers have dominated the work place while the others in modern days have microprocessors.

All these electronic equipment when connected to the power system can actually generate electrical disturbances, which can adversely affect other equipment within the power network. Heavy industrial equipment such as nonlinear variable speed drives powered through power electronic converters cause the power disturbances. The transient problems such as sags and swells when repeatedly experienced can damage electronic equipment connected to the network.

These problems are sometimes unpredictable, because they may happen any where at any time. High-speed and modern communications allow internet monitoring. Monitoring the system needs to be done periodically at regular interval, otherwise it may become too late to take a corrective action and save the equipment connected to the system.

Both electric utilities and end users of electrical power are becoming increasingly concerned about the quality of electric power. There are many reasons for this growing concern such as; the use of microprocessor-based controls and power electronic devices. In fact many loads are sensitive to different types of disturbances.

Any power problem is manifested in voltage, current, or frequency deviations. A failure or misoperation of customer equipment means that there is a power quality problem.

3. STEADY STATE COMPENSATOR

There are several FACTS devices used in power system, some of them are already installed in the power system and in operation. Those devices can be categorized into 3 groups. One group in category is series compensators - TCSR, TCSC. Another group is shunt compensators such as SVC, STACOM. The last one is combined compensators like UPFC.

Among those compensators, series compensators are adapted in [14] because it shows highly cost efficient characteristics in controlling the active power flow.

The use of FACTS devices to improve the power transfer capability in a high voltage transmission line is of greater interest these days [15]. In [16] a residue factor method used to obtain the optimal location of TCSC device to damp out the inter-area mode of oscillations.

3.1 Theory

In the case of a no-loss line, voltage magnitude at receiving end is the same as voltage magnitude at sending end:

$$V_s = V_r = V. \quad (1)$$

Transmission results in a phase lag δ that depends on line reactance X .

As it is a no-loss line, active power P is the same at any point of the line:

$$\begin{aligned} P_s = P_r = P & \quad (2) \\ & = (V \cos(\delta/2))((2V \sin(\delta/2))/X) \\ & = (V^2 \sin(\delta))/X \quad (3) \end{aligned}$$

Reactive power at sending end is the opposite of reactive power at receiving end:

$$\begin{aligned} Q_s = -Q_r = Q & \\ & = (V \sin(\delta/2))((2V \sin(\delta/2))/X) \\ & = (V^2(1-\cos(\delta)))/X \quad (4) \end{aligned}$$

As δ is very small angle, active power mainly depends on δ whereas reactive power mainly depends on voltage magnitude.

3.2 Series FACTS

FACTS for series compensation modify line impedance: the total transmission system reactance X is decreased so as to increase the transmittable active power. However, more reactive power must be provided.

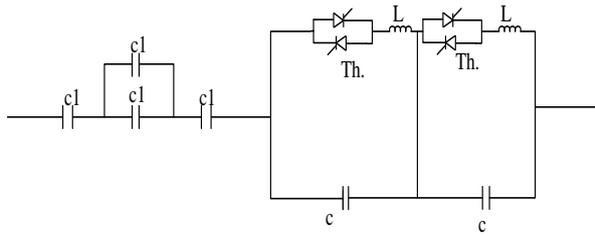
$$P = (V^2 \sin(\delta))/(X - X_c) \quad (5)$$

$$Q = (V^2(1-\cos(\delta)))/(X - X_c) \quad (6)$$

This can be done by one of the following two methods.

3.2.1 Advanced thyristor controlled series capacitor (ATCSC):

It is a capacitive reactance compensator which consists of a series capacitor bank shunted by thyristor controlled reactor TCR, in order to provide a smoothly variable series capacitive reactance X_c . ATCSC components are given in Fig. 1.

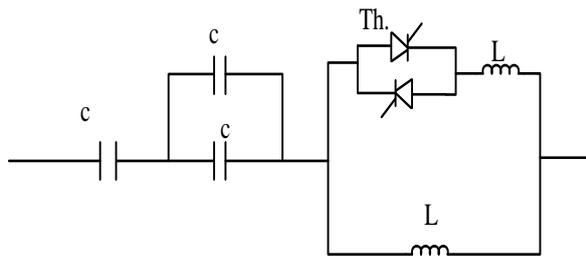


$c=1F, c1=0.1F, L=1H, Th.: Vig=3V, Ihold=0.1A, Tdeion=0.0001sec.$

Fig.1 Advanced Thyristor Controlled Series Capacitor (ATCSC)

3.2.2 Advanced thyristor controlled series reactor (ATCSR):

It is an inductive reactance compensator, which is composed of a series reactor, shunted by a thyristor-controlled reactor TCR, in order to provide a smoothly variable series inductive reactance. Series fixed reactor is provided to have continual service during control periods. ATCSR components are given in Fig.2.



$c=0.07F, L=0.001H, Th.: Vig=3V, Ihold=0.1A, Tdeion=0.0001sec.$

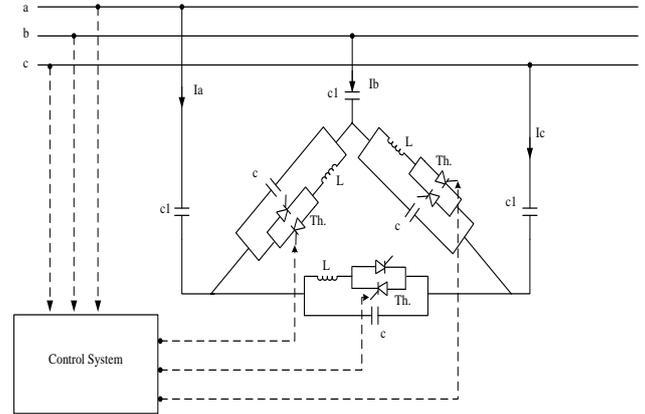
Fig. 2 Advanced Thyristor Controlled Series Reactor (ATCSR)

3.3 Shunt FACTS

Reactive current is injected into the line to maintain voltage magnitude. Transmittable active power is increased but more reactive power is to be provided.

3.3.1 Advanced static VAR compensators (ASVC):

The thyristor-controlled shunt compensator is called Advanced Static VAR compensator (ASVC). The ASVC has the big advantages of fast response and energy conversion with low losses. ASVC components are given in Fig.3.

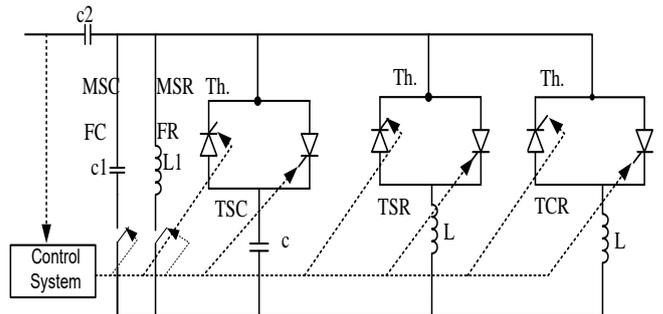


$c=0.8F, c1=0.045F, L=1H, Th.: Vig=3V, Ihold=0.1A, Tdeion=0.0001sec.$

Fig. 3 Advanced Static VAR Compensators (ASVC)

3.3.2 Advanced static VAR system (ASVS):

It is Advanced Static VAR System (ASVS) as a combination of Advanced Static VAR Compensator (ASVC) and mechanically switched capacitor (MSC) and mechanically switched reactors (MSR). ASVS components are given in Fig.4.



$c=0.8F, c1=1*10^{-20}F, L=1H, L1=1*10^{-6}H Th.: Vig=3V, Ihold=0.1A, Tdeion=0.0001sec.$

Fig.4 Advanced Static VAR System (ASVS)

4. SYSTEM UNDER STUDY SIMULATION

4.1 IEEE 30 bus system

Fig. 5 shows IEEE 30 bus system under study. ATP simulation program is used. The ratings of FACTS components enter to ATP program used are shown in Figs. 1,2,3 and 4. Fig. 6 shows the voltage at bus 3 at normal state from 0 to 0.2 sec. (voltage=27.30 Kv) and when the generator connected to bus 1 is out of service from 0.2 to 0.4 sec. (voltage=16.25 Kv) and when connected Advanced Thyristor Controlled Series Capacitor (ATCSC) at bus 3 from 0.4 to 0.6 sec. and the voltage increased to the normal state (voltage=27.30 Kv). Fig.7 shows the voltage at bus 3 at normal state from 0 to 0.2 sec. and when generator connected to bus 1 is out of service from 0.2 to 0.4 sec. and when connected Advanced Thyristor Controlled Series Reactor (ATCSR) at bus 3 from 0.4 to 0.6 sec. and the voltage increased to the normal state (voltage=27.30 Kv). Fig. 8 shows the voltage at bus 3 at normal state from 0 to 0.2 sec. and when generator connected to bus 1 is out of service from 0.2 to 0.4 sec. and when connected Advanced Static VAR System

(ASVS) at bus 3 from 0.4 to 0.6 sec. and the voltage increased to the normal state (voltage=27.30 Kv). Fig.9 shows the voltage at bus 3 at normal state from 0 to 0.2 sec. and when generator connected to bus 1 is out of service from 0.2 to 0.4 sec. and when connected Advanced Static VAR Compensator (ASVC) at bus 3 from 0.4 to 0.6 sec. and the voltage increased (voltage=27.30 Kv).

In Fig.6 and Fig.7 series FACTS are connected in system and connect ATCSC when needed smoothly variable series capacitive reactance in system and connect ATCSR when needed smoothly variable series inductive reactance in system. In Fig. 8 and Fig.9 shunt FACTS are connected in system and connect ASVS when needed to connect mechanically switched capacitor (MSC) and mechanically switched reactors (MSR) in system. Voltage is offset when generator connected to bus 1 is out of service and after connect FACTS because of DC component in network and generator connected to bus 1 (out of service) is designed to absorb DC component in network.

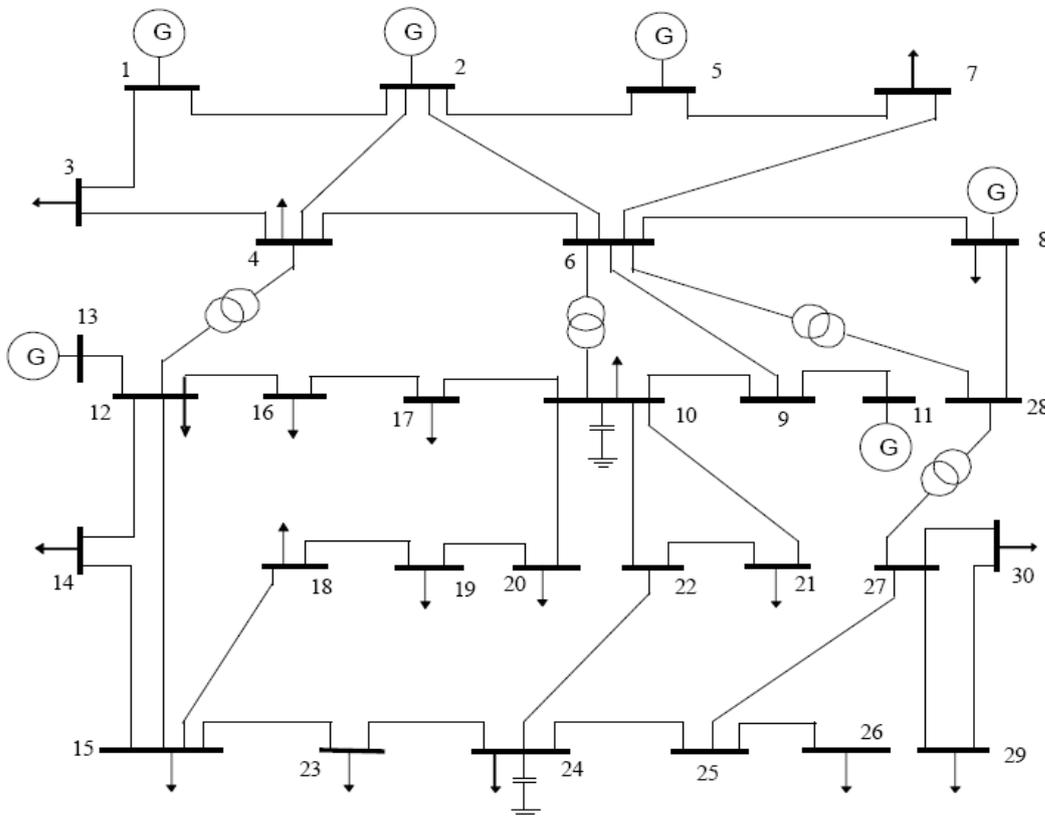


Fig. 5 IEEE 30 bus system

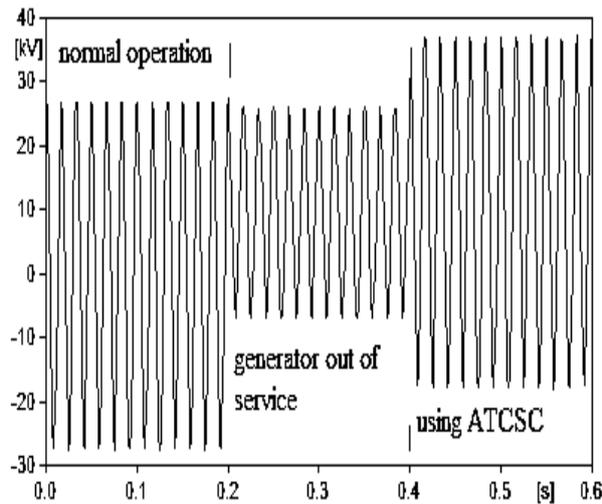


Fig. 6 The Voltage at Bus 3 at Normal State in Case of Generator out of Service, Using (ATCSC)

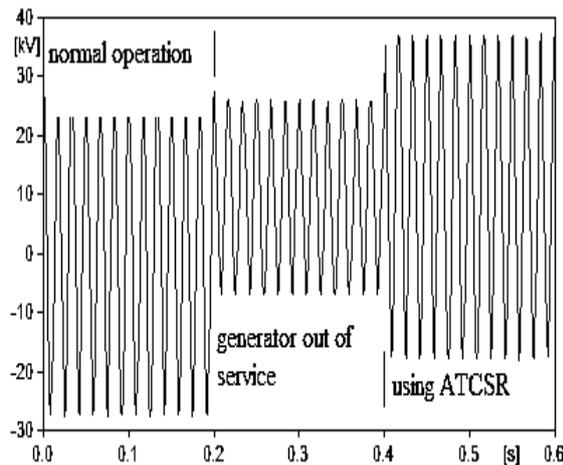


Fig.7 The Voltage at Bus 3 at Normal State in Case of Generator out of service, Using (ATCSR)

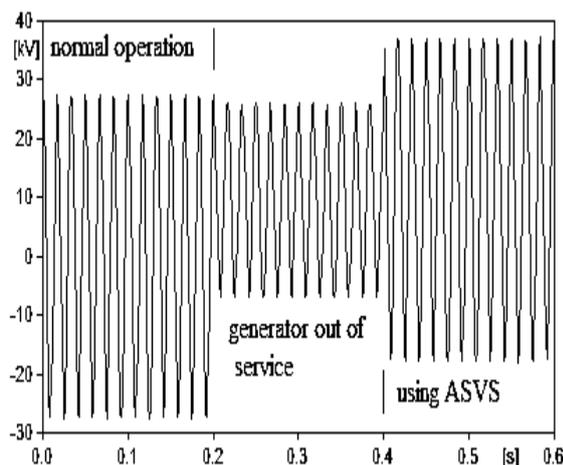


Fig. 8 The Voltage at Bus 3 at Normal State in Case of Generator out of Service, Using (ASVS)

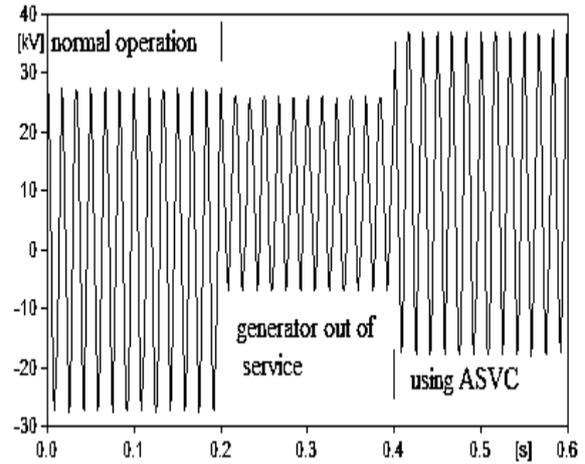


Fig. 9 The Voltage at Bus 3 at Normal State in Case of Generator out of service, Using (ASVC)

Fig.10 shows the voltage at bus 2 at normal state from 0 to 0.2 sec. (voltage=27.3Kv) and when generator connected to bus 2 is out of service from 0.2 to 0.4 sec. and the voltage is decreased (voltage=11.2Kv) and when connected Advanced Thyristor Controlled Series Capacitor (ATCSC) at bus 2 from 0.4 to 0.6 sec. and the voltage increased to the normal state (voltage=27.30 Kv).

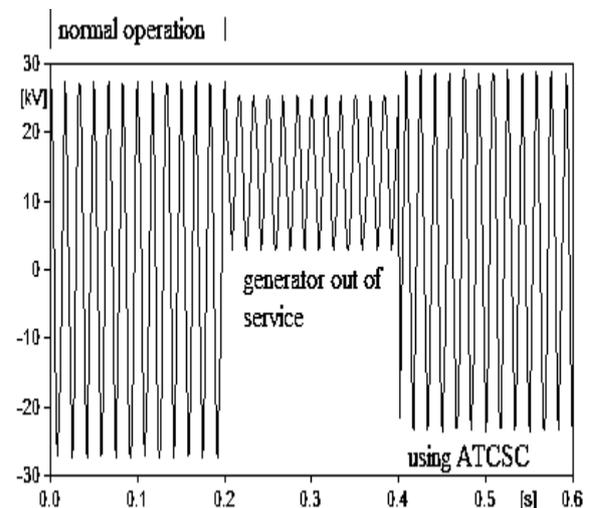


Fig.10 The Voltage at Bus 2 at Normal State in Case of Generator out of Service, Using (ATCSC)

Fig. 11 shows the voltage at bus 2 at normal state from 0 to 0.2 sec. and when generator connected to bus 2 is out of service from 0.2 to 0.4 sec. and when connected Advanced Thyristor Controlled Series Reactor (ATCSR) at bus 2 from 0.4 to 0.6 sec. and the voltage increased to the normal state (voltage=27.3 Kv).

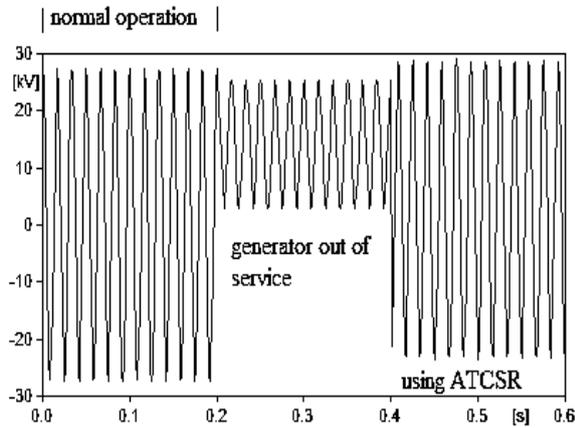


Fig.11 The Voltage at Bus 2 at Normal State in Case of Generator out of Service, Using (ATCSR)

Fig.12 shows the voltage at bus 2 at normal state from 0 to 0.2 sec. and when generator connected to bus 2 is out of service from 0.2 to 0.4 sec. and when connected Advanced Static VAR System (ASVS) at bus 2 from 0.4 to 0.6 sec. and the voltage increased to the normal state (voltage=27.3Kv).

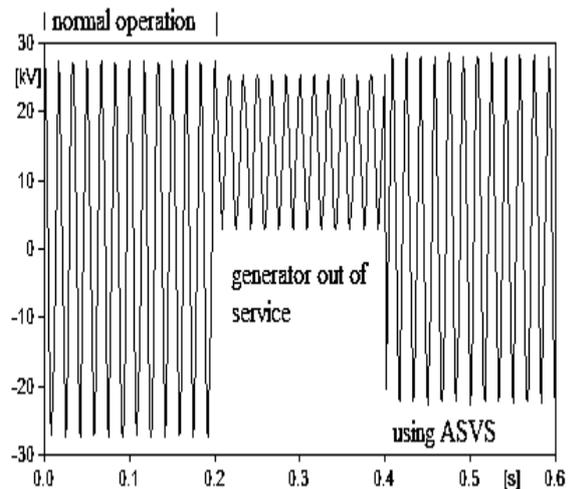


Fig.12 The Voltage at Bus 2 at Normal State in Case of Generator out of Service, Using (ASVS)

Fig.13 shows the voltage at bus 2 at normal state from 0 to 0.2 sec. and when generator connected to bus 2 is out of service from 0.2 to 0.4 sec. and when connected Advanced Static VAR Compensator (ASVC) at bus 2 from 0.4 to 0.6 sec. and the voltage increased (voltage=27.30 Kv).

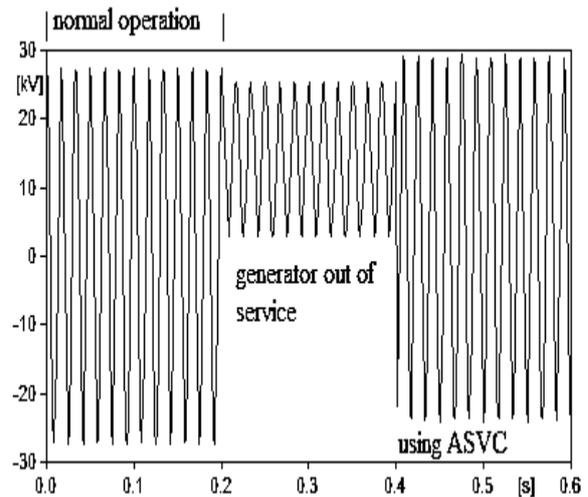


Fig.13 The Voltage at Bus 2 at Normal State in Case of Generator out of Service, Using (ASVC)

In Fig.10 and Fig.11 series FACTS are connected in system and connect ATCSR when needed smoothly variable series capacitive reactance in system and connect ATCSR when needed smoothly variable series inductive reactance in system. In Fig.12 and Fig.13 shunt FACTS are connected in system and connect ASVS when needed to connect mechanically switched capacitor (MSC) and mechanically switched reactors (MSR) in system. Voltage is offset when generator connected to bus 2 is out of service and after connect FACTS because of DC component in network and generator connected to bus 2 (out of service) is designed to absorb DC component in network.

5. CONCLUSION

In this paper, ATCSR, ATCSR, ASVS, and ASVC are only used to study for the IEEE 30 bus power system. The optimal location to connect FACTS is the bus need to improve power quality. Rating of these FACTS devices to improve the power quality are shown in Figs. 1,2,3 and 4. The ATP simulation program have been used for simulation. The Alternative Transient Program (ATP) techniques use power flow calculation has several advantages such as fast calculation, possibility of determination of the margin of power quality problem. The best type of FACTS used to improve power quality is ATCSR because of fixed series capacitors shunted by thyristor controlled reactor are provided so as to guarantee service continuity during control actions.

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